TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

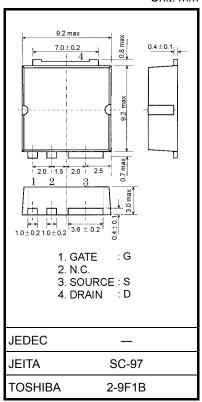
2SK3397

Relay Drive and DC-DC Converter Applications Motor Drive Applications

- Low drain-source ON resistance: $RDS(ON) = 4.0 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 110 \text{ S} (typ.)$
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 30 \ V)$
- Enhancement mode: $V_{th} = 1.5$ to 3.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	۱ _D	70	А
	Pulse (Note 1)	I _{DP}	210	A
Drain power dissipation (Tc = 25° C)		PD	125	W
Single pulse avalanche energy (Note 2)		E _{AS}	273	mJ
Avalanche current		I _{AR}	70	А
Repetitive avalanche energy (Note 3)		E _{AR}	12.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to150	°C



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.0	°C/W

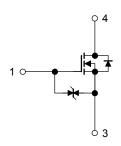
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 40 μ H, I_{AR} = 70 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Circuit Configuration



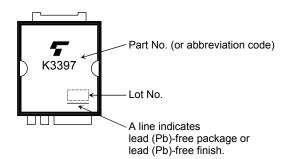
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_	_	±10	μA
Drain cut-OFF current		I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		_	10	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$ 30				V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	v
Gate threshold voltage		V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	1.5	_	3.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 35 \text{ A}$	_	4.0	6.0	mΩ
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 35 \text{ A}$	55	110	_	S
Input capacitance		C _{iss}		_	5000	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{MHz}$	_	550	_	
Output capacitance		C _{oss}		_	1000	_	
Switching time	Rise time	tr	$V_{GS}^{10 V} \downarrow_{D} = 35 \text{ A} \\ 0 V \downarrow_{GS}^{C} \downarrow_{D} \neq 0 \\ V_{DD} \approx 15 \text{ V}$ $V_{DD} \approx 15 \text{ V}$	_	8.0	_	• ns
	Turn-ON time	t _{on}		_	25	_	
	Fall time	t _f		_	48	_	
	Turn-OFF time	t _{off}			180	_	
Total gate charge (gate-source plus gate-drain)		Qg			110		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 24$ V, $V_{GS} = 10$ V, $I_D = 70$ A	_	87	—	
Gate-drain ("miller") charge		Q _{gd}			23		

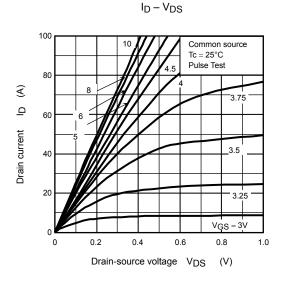
Source-Drain Ratings and Characteristics (Ta = 25°C)

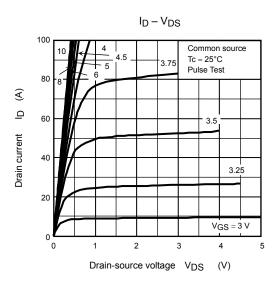
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	70	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	210	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 70 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 70 \text{ A}, V_{GS} = 0 \text{ V},$	_	40	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 30 A/µs	_	40	_	nC

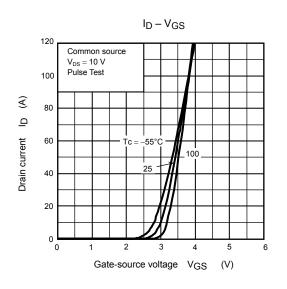
Marking

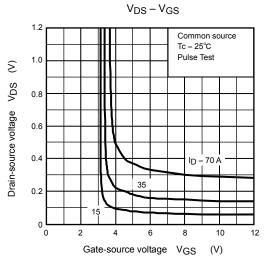


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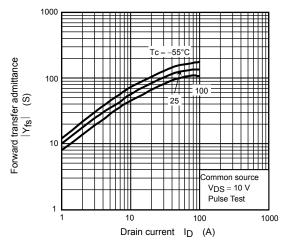




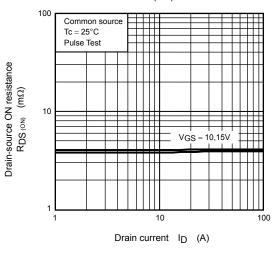




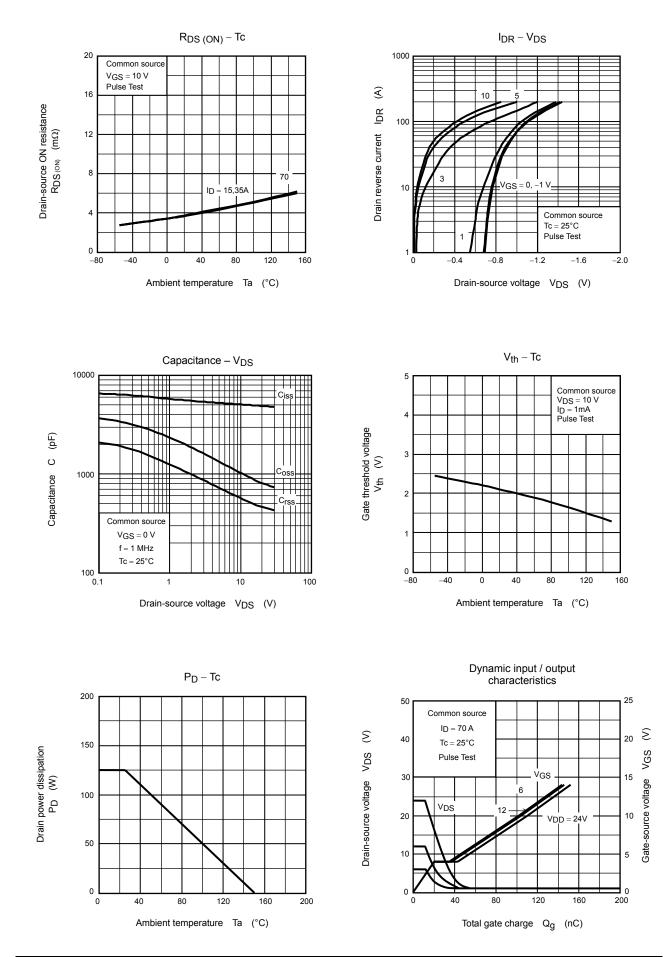


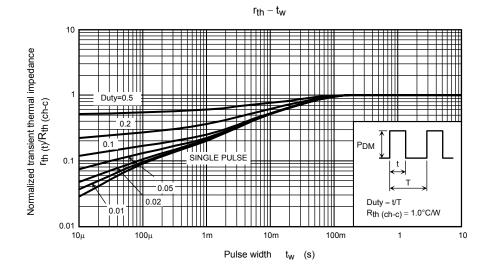


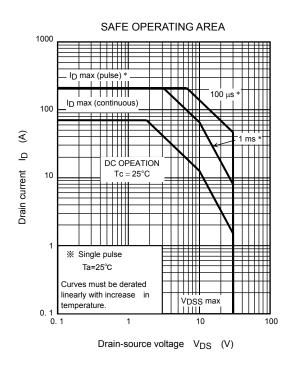




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 $E_{AS} - T_{ch}$ 500 (ſш) 400 Avalanche energy EAS 300 200 100 0 L 25 50 75 100 125 150 Channel temperature (initial) T_{ch} (°C) **B**_{VDSS} 15 V –15 V V_{DD} VDS Test circuit Wave form $E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$ R_G=25 Ω

 $V_{DD} = 25 V, L = 40 \mu H$

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